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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Ivan Yang et al.

Docket No: 0100.01272

Title: METHOD AND APPARATUS FOR CONTROLLING DISPLAY OF CONTENT SIGNALS

To the Honorable Commissioner
of Patents and Trademarks
Box Patent Application
Washington, D.C. 20231

Date: October 5, 1998

TRANSMITTAL LETTER FOR FILING A NATIONAL PATENT APPLICATION

The applicants hereby respectfully request that the above captioned patent application be accepted for examination. This patent application is a:

- ☒ new patent application;
- ☐ continuation in part (CIP) of Application Serial No. _____ filed on _____;
- ☐ divisional application of Application Serial No. _____ filed on _____;
- ☐ continuation of Application Serial No. _____ filed on _____;

Accompanying this request is (as indicated by an "X" in the corresponding box):

- ☒ 1. 19 pages of specification, which includes the claims and abstract, and 5 sheets of formal drawings;
- ☒ 2. Combined Declaration and Power of Attorney;
- ☒ 3. An Information Disclosure Statement along with the references;
- ☐ 4. A petition to extend the response for a priority application identified above;
- ☒ 5. An assignment assigning all rights in the above referenced patent application to ATI International SRL;
- ☒ 6. An assignment recording cover sheet;
- ☐ 7. A verified statement establishing small entity status under 37 C.F.R. Sections 1.9 and 1.27;

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- ☒ 8. A certificate of mailing indicating that the above captioned patent application has been deposited as "Express Mail" with the United States Postal Service;
- ☐ 9. A certificate of mailing indicating that the above captioned patent application has been deposited with the United States Postal Service with sufficient postage as first class mail;
- ☒ 10. A return postcard; and
- ☐ 11. A preliminary amendment.

The filing fee for the above captioned patent application is as follows:

	Data Entry	
Basic filing fee	xxxxx	\$ 790.00
Total No. of Claims	22	\$ 44.00
Total No. of Ind. Claims	3	\$ -
Assignment Recording Fee	xxxxx	\$ 40.00
Total Filing Fee		\$ 874.00

Payment of the above calculated filing fee is as follows (as indicated by the "X" in the corresponding box):

- ☐ A check in the amount of \$;
- ☒ Please charge Deposit Account No. 50-0441 in the amount of \$874.00. A duplicate sheet is attached. The Commissioner is hereby authorized to charge or credit any additional amounts regarding this patent application to the same Deposit Account.

Respectfully submitted,

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85809T-2209160

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**METHOD AND APPARATUS FOR
CONTROLLING DISPLAY OF CONTENT SIGNALS**

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Technical Field of the Invention

This invention relates generally to video and audio processing systems and more particularly to controlling display of video signals and/or audio signals.

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Background of the Invention

The entertainment industry is producing an ever-increasing number of video and/or audio products for private and public use. Today, a consumer may rent or purchase a movie in a videocassette format, a laser disk format, a DVD format, etc. In addition, the user may view a movie via a television broadcast, cable broadcast, and/or satellite broadcast. Similarly, for audio products, a consumer may purchase audiocassettes, CDs, or sound tracks or listen to radio and/or satellite audio broadcasts. In addition to the numerous sources of audio and/or video materials, the content of such merchandise is more explicit than in the past. As such, the debate over how, and who, should censor such material is increasing.

The U.S. movie industry has addressed the censorship issue by providing ratings to the movies it produces based on the audio and/or video content. Such ratings include: G indicating that the content is acceptable for general audiences, PG indicating that parental guidance is suggested because some of the content may be too mature for all persons; PG-13 indicating that the movie may be inappropriate for children under 13; R indicating that the movie is restricted such that no one under the age of 17 may view the

movie in a theater without his or her parents; R-17 indicating that no one under the age of 17 is allowed to view the movie in a theater, and X indicating the content is explicitly for adults. Based on these ratings, parents and/or guardians may monitor the movies in which they allow their children to view at a movie theater. While the rating system works fairly well to restrict younger viewers from viewing inappropriate movies at public theaters, it only provides guidance to parents and/or guardians for publicly displayed movies.

With the increased number of sources for providing audio and/or video content into the home, such as cable, satellite, VCR tapes, laser disks, DVD disks, etc., the challenge to monitor a child's viewing choices is becoming an increasingly difficult task. To address this issue, the United States has undertaken a quest to establish a standard that would enable a parent to automatically restrict their children's viewing. Such a standard revolves around parental control technology. In essence, parental control technology allows a parent to enable a video and/or audio source receiver (e.g., a video cassette player, television, cable box, satellite receiver, etc.) to restrict the showing of movies that the parent has deemed inappropriate. As such, the parent can set a parental control device to any desired level of censoring.

The parental control regulation, as issued by the United States, has established that at least a portion of manufactured video and/or audio source receivers shall be equipped with the parental control technology, ramping up to full compliance over the next few years. Thus, any manufacturer that produces a video and/or audio source receiver, which includes computers equipped with a video tuner and/or encoder, must be compliant with this standard.

Therefore, a need exists for a method and apparatus that enables a computer that is capable of presenting video, audio and/or text information to be compliant with the parental control standard.

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Brief Description of the Drawing

Figure 1 illustrates a schematic block diagram of a computing system in accordance with the present invention;

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Figure 2 illustrates a schematic block diagram of an alternate computing system in accordance with the present invention;

Figure 3 illustrates schematic block diagrams of an audio and video scrambler in accordance with the present invention;

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Figure 4 illustrates a schematic block diagram of a video graphics engine in accordance with the present invention; and

Figure 5 illustrates a logic diagram of a method for controlling display of content signals in accordance with the present invention.

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Detailed Description of a Preferred Embodiment

Generally, the present invention provides a method and apparatus for controlling display of content signals. Such processing begins by receiving a content signal that includes video, audio, and/or text content and at least one associated content control indicator. Note that a separate content control indicator may accompany video, audio, and text portions of the content signal. The processing continues by comparing the at least one associated content control indicator (e.g., a rating of mature subject matter of the content signal) with at least one content control setting (e.g., a parental setting based on allowable viewing of rated content signals). When the associated content indicator compares unfavorably to the content control setting, a video graphics processor scrambles the at least a portion of the video content. The scrambled video content is then provided to a video rendering device for subsequent display. With such a method and apparatus, a

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video graphics controller and/or personal computer may provide parental control regarding content signals that include video, audio, and/or text content, from being displayed and/or recorded with objectionable mature content. By utilizing the content control setting, a user may set individual settings based on his or her preferences and the level of censoring that he or she desires for his or her children.

The present invention can be more fully described with reference to Figures 1 through 5. Figure 1 illustrates a schematic block diagram of a computing system 10 in accordance with the present invention. The computing system 10 may be a personal computer, a cable box, television, satellite receiver, DVD player, VCR player, video game, etc. The computing system 10 includes a tuner 14, an audio scrambler module 16, an audio decoder 18, an audio processing module 20, a video scramble module 22, a video decoder 24, a graphics controller 26, a central processing unit 28, and system memory 30. The computing system 10 is operably coupled to receive content signals 46 from a video source 12 and is further operably coupled to an audio rendering device 32 and a video rendering device, such as a display 34 and/or a recorder 36. The video source may be a VCR player, DVD player, television broadcast, cable broadcast, satellite broadcast, laser disk player and/or any device that sources text content, video content and/or audio content to the computing system 10.

The content signal includes video, audio, and/or text content and at least one content control indicator. If the content signal includes a single content control indicator, the content control indicator applies to the entire content. For example, if the content control indicator is set to a movie rating of R, the entire content (video, audio, and/or text) will be based on this rating. Alternatively, the content signal may include a plurality of content control indicators associated with various segments of video, text, and/or audio content. In this case, the content control indicator only regulates the corresponding portion of the video, text, and/or audio content. As such, the content signal is selectively controlled and/or censored by the associated content control indicator.

The tuner 14, which may be a tuner as found in ATI Technology's All-in-Wonder board, receives the content signal 46 and separates the video content and audio content producing a baseband audio signal 48 and a baseband video signal 50. The audio scramble model 16 receives the baseband audio signal and scrambles the baseband audio signal as indicated by the scramble control signal 52. Similarly, the video scramble module 22 receives the baseband video signal 50 and scrambles it based on the scramble control signal 52.

The graphics controller 26 generates the scramble control signal 52 via a content controller 40. The content controller 40 includes a processing module 42 and memory 44. The processing module 42 may be a single processing entity or a plurality of processing entities. Such a processing entity may be a microprocessor, microcontroller, microcomputer, central processing unit, digital signal processor, video graphics processor, state machine, logic circuitry, and/or any device that manipulates digital information based on programming and/or operational instructions. The memory 44 may be a single memory device or a plurality of memory devices. Such a memory device may be a read only memory device, random access memory, floppy disk, hard drive memory, external memory, magnetic tape memory, DVD memory, CD memory, and/or any device that stores operational and/or programming instructions. Note that if the processing module implements one or more of its functions via a state machine or logic circuitry the operational instructions stored in memory are embedded within the circuitry comprising the state machine and/or logic circuitry. The operational instructions stored in memory 44 and executed by processing module 42 will be discussed in greater detail with reference to Figure 5.

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In general, the content controller 40 receives and/or monitors the video signal 46, the baseband video signal 50, the decoded video signal 56 and/or the video output 60 to detect the content control indicator. Typically, the content controller 40 monitors the baseband video signal for the content control indicator, however, any other of these signals may be used to detect the content control indicator. As one of average skill in the

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art would appreciate, if the video output 60 is used to monitor for the content control indicator, a delay mechanism would need to be employed in the content controller to properly delay the associated video and/or audio content.

5 The audio scramble module 16 and the video scramble module 22 are operable to scramble the respective baseband audio signals 48 and the baseband video signals 50 when the scramble control signal 52 is active. When the scramble control signal 52 is not active, the audio scramble module 16 and video scramble module 22 pass their respective signals unaltered.

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 The audio decoder 18 is operably coupled to receive the output from the audio scramble module 16. The audio decoder 18 produces a decoded audio signal 54, which is typically in an analog format, and provides it to an audio processing module 20. The audio decoder 18 may be the type found in ATI Technology's All-in-Wonder board. The
15 audio processing module 20 receives a decoded audio signal, which may be the original audio content or scrambled audio content, and produces an audio output 58 therefrom. The audio processing module 20 may be a sound card as found in computers. The output signal 58 is provided to an audio rendering device 32, which may be an analog-digital SPDIF, a speaker or plurality of speakers, and renders the audio output 58 audible. As
20 such, in the audio path, if the content controller 40 detects the content control indicator and determines it exceeds a content control setting, the associated portion of the audio content will be scrambled thereby providing control over the level of explicit content that will be rendered audible.

25 The video decoder 24, which may be of the type found in ATI Technology's All-in-Wonder board, receives the output from the video scramble module 22 and produces decoded video 56 therefrom. The decoded video is provided to a video processing engine 38 of the graphics controller 26 and may also be received by the central processing unit 28 (e.g., record the decoded video). The central processing unit 28 may process the
30 decoded content signal by storing it in the system memory 30 or in another memory

device associated with the computing system 10. The video processing engine 38 receives the decoded video 56 which may include the original video content or a scrambled representation thereof, and produces a video output 60. The video output is provided to the video-rendering device, which may be a display 34 and/or a recorder 36.

5 As such, the content controller 40, when the content control indicator associated with the content signal exceeds the content control setting, controls associated portions of the video content or the entire video content by scrambling the baseband video signal 50 or the decoded video 56. As such, parental control is provided for video content.

10 Figure 2 illustrates a schematic block diagram of an alternate computing system 70 in accordance with the present invention. In this embodiment, the computing system 70 includes the audio scramble module 16 coupled to the output of the audio decoder 18. In addition, the video scramble module 22 is coupled to the output of the video decoder 24 or to the output of the video processing engine 38. In this embodiment, the decoded
15 audio signal 54 is typically an analog signal, such that the audio scramble module 16 operates in the analog domain. Similarly, the video scramble module 22 may be acting upon an analog representation of the video 60, or, if the display is a digital display, acting upon a digital representation of the video 60. As such, as shown in Figures 1 and 2, the audio scramble module 16 and the video scramble module 22 may be digital and/or
20 analog devices. As one of average skill in the art would appreciate, the video scramble module 22 may further be incorporated as part of the graphics controller and/or within the video processing engine 38, which will be discussed with reference to Figure 4.

The analog path of the computing system 70 further may include an audio
25 attenuation module 72. In general, if the audio signal is scrambled, it may produce, if unattenuated, an audible signal that would be unpleasant to the user. As such, by including an audio attenuation module 72, which is activated based on the scramble control signal 52, the scramble audio signal may have reduced volume.

Figure 3 illustrates several schematic block diagrams of the audio and/or video scramble module 16 and 22. In the first embodiment, the scramble module 16 or 22 includes a digital mixer 80 that is operably coupled to receive the digital audio signal 48, the decoded audio 54, the digitized content signal 50, or the decoded video 56. The
 5 scramble module further includes a digital phase shift module 82 operably coupled to also receive the input of the mixing signal. The phase shift module 82 shifts the signal (e.g., by 180 degrees +/- 90 degrees), which is mixed with the original signal when the scramble control signal 52 is active. As such, when the scramble control signal 52 is active the mixer 80 is mixing the input signal with a phase shifted representation of the
 10 input signal thereby producing a scrambled output.

As an alternate scramble module 16 or 22, the mixer 80 is operably coupled to receive the corresponding digital input signal and also to receive a scramble signal via scramble module 84. When the scramble control signal 52 is active, the scramble signal
 15 is provided to the mixer 80. The scramble signal may be generated as a random number generator, based on the digital input signal, or any other scrambling technique that would produce a video output that is unintelligible.

An analog audio scramble module 16 is also shown to include a summing module
 20 86 that is operably coupled to receive an audio analog input and a phase shifted representation thereof when the scramble control signal 52 is active. As such, by subtracting a phase shifted analog representation of the analog signal from the analog signal the resulting output is sufficiently scrambled to render the audio outputs
 25 unintelligible.

Figure 3 further shows an analog audio scramble module 16 that includes a mixing node 88, an attenuation module 92 and a scramble signal generator 90. When the scramble control signal 52 is active, the scramble signal generator 90, which may produce a sinusoidal signal, is mixed with the analog input by mixer 88. The
 30 unintelligible output may then be attenuated via the attenuation module 92. The

attenuation module may be linearly controlled by the scramble control signal or function in an off/on mode as controlled by the scramble control signal 52.

Figure 4 illustrates a schematic block diagram of the video processing engine 38 that may be used for a two dimensional pipeline process and/or a three dimensional pipeline process. The video processing engine 38 includes a culling module 102, a Z buffer module 104, a setup module 106, a scramble module 100, a rendering module 108, and a frame buffer 110. The output of the frame buffer 110 provides the video output 60 to a display driver 112. The culling module 102 is operably coupled to receive geometric information 114 regarding the content signals to be displayed. From this information, the culling module produces Z values 116 and culling information 118. The Z values 116 are processed by the Z buffer module 104 and outputted as Z information 122. The culling information 118 is processed by the setup module 106 to produce setup information 120. The rendering module 108 is operably coupled to receive the Z information 122 and the setup information 120 and produces therefrom rendered images 124. The rendered images are then stored in the frame buffer 110 and subsequently provided to a display via a display driver 112.

The scramble module 100 is operably coupled to the setup module 106 and/or the rendering module 108. When coupled to the setup module 106, the scrambling module 100, when activated via the scrambled control signal 52, provides the setup module with information that causes it to scramble the setup information 120. For example, the scramble information provided to the setup module 106 may alter the physical coordinates of the triangles being rendered and/or the texture coordinates. In addition, the scramble information may alter the color data such that when rendered, the images are unintelligible.

When the scramble module 100 is coupled to the rendering module 108, the scramble information provided to the rendering module 108 may cause the rendered images 124 to be stored in the frame buffer in a random manner. As such, the scramble

information provided to the rendering module may be random numbers that are added to, subtracted from, or otherwise mathematically used to manipulate the addresses of the frame buffer 110. As such, even though the proper pixel information is being generated, it is scrambled throughout the frame buffer such that the resulting video image is

5 unintelligible. As one of average skill in the art would appreciate, there are numerous ways in which the scramble module may alter the function of the video graphics engine to produce a video output 60 that is unintelligible.

Figure 5 illustrates a logic diagram of a method for controlling display of content signals. The process begins at step 130 where a content signal is received, wherein the content signal includes at least one associated content control indicator and video content. Note that the content signal may further include audio content and/or closed captioned content. The process then branches to steps 132 and 142. At step 132, the content control indicator is compared with at least one content control setting, which is provided

15 by the user of the computing system. For example, if the user is a parent trying to regulate the mature content of information that his or her child may view via the computing system, the content control setting would reflect the desired mature nature of the subject matter that the parent wishes his or her child to view.

20 The process then proceeds to step 134 where a determination is made as to whether the comparison is favorable. A favorable comparison is one in which the content control indicator is at a rating below the content control setting. In other words, the content control indicator indicates that the corresponding video content, audio content, and/or close captioned content is acceptable for unaltered display. If the comparison was

25 favorable, the process proceeds to step 136 where the content signal is processed without scrambling.

If, however, the comparison at step 134 was not favorable, the process proceeds to step 138. At step 138, at least a portion of the video content is scrambled to produce

30 scrambled video content. In addition, if the content signal included audio content it

would also be scrambled as well as any corresponding closed captioned content. In addition to scrambling the audio signal, the scrambled audio signal may be attenuated such that the resulting audible output is minimized. As an alternate to scrambling the audio signal, an audio scramble signal may be provided to an audio processing module
 5 such that the audio processing module, or audio card, may perform the scramble function. The process then proceeds to step 140 where the scrambled video content, audio content, and/or closed captioned content are provided to the respective video rendering device and/or audio rendering device.

10 At step 142, the content control indicator is interpreted to determine copy restriction status. Such copy restriction status includes allowing no copying, allowing single copying, or multiple copies. The process then proceeds to step 144 where a determination is made as to whether the copy restriction is enabled. If not, the process proceeds to step 146 where the content signal may be copied. If the copy restriction is
 15 enabled, the process proceeds to step 148 where the copying of the video content, audio content and/or close captioned content is prevented.

The preceding discussion has presented a method and apparatus for controlling display of content signals. By scrambling video content, audio content, and/or close
 20 caption content, when a corresponding content control indicator compares unfavorably to a user content control setting, control of the displaying of objectionable material is obtained. In addition, by utilizing a scrambling technique, the video graphics circuitry functions in a similar manner to produce a scramble output and/or the unscrambled output. Thus, minimal additional circuitry is required to achieve parental control.-

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Claims

What is claimed is:

- 5 1. A method for controlling display of content signals, the method comprises the steps of:
- a) receiving a content signal that includes at least one of video, audio, and text content and at least one associated content control indicator;
- 10 b) comparing the at least one associated content control indicator with at least one content control setting;
- when the at least one associated content control indicator compares unfavorably to the at
- 15 least one content control setting:
- c) scrambling at least a portion of the at least one of video, audio, and text content to produce scrambled content; and
- 20 d) providing the scrambled content to a content rendering device.
2. The method of claim 1 further comprises:
- scrambling at least a portion of the audio content to produce scrambled audio content,
- 25 wherein the content signal includes the audio content; and
- providing the scrambled audio content to an audio rendering device.
3. The method of claim 2 further comprises attenuating the at least a portion of the
- 30 audio content to produce the scrambled audio content.

4. The method of claim 1 further comprises:

scrambling the text content to produce scrambled text content, wherein the content signal
5 includes the text content; and

providing the scrambled closed captioned content to a display.

5. The method of claim 1 further comprises:

10 interpreting the at least one associated content control indicator to determine copy
restriction status; and

when copy restriction is enabled, preventing copying of the content signal.

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6. The method of claim 1 further comprises:

providing an audio scrambling signal to an audio processing module when the at least one
associated content control indicator compares unfavorably to the at least one content
20 control setting.

7. A content controller comprises:

a processing module; and

5 memory operably coupled to the processing module, wherein the memory stores operational instructions that cause the processing module to (a) receive a content signal that includes at least one of video, audio, and text content and at least one associated content control indicator; (b) compare the at least one associated content control indicator with at least one content control setting;

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when the at least one associated content control indicator compares unfavorably to the at least one content control setting; (c) scramble at least a portion of the at least one of video, audio, and text content to produce scrambled content; and (d) provide the scrambled content to a content rendering device.

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8. The content controller of claim 7, wherein the memory further comprises operational instructions that cause the processing module to:

scramble at least a portion of the audio content to produce scrambled audio content,

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wherein the content signal includes the audio content; and

provide the scrambled audio content to an audio rendering device.

9. The content controller of claim 8, wherein the memory further comprises operational instructions that cause the processing module to attenuate the at least a portion of the audio content to produce the scrambled audio content.

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10. The content controller of claim 7, wherein the memory further comprises operational instructions that cause the processing module to:

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scramble at least a portion of the text content to produce scrambled text content, wherein the content signal includes the text content; and

provide the scrambled text content to the display.

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11. The content controller of claim 7, wherein the memory further comprises operational instructions that cause the processing module to:

interpret the at least one associated content control indicator to determine copy restriction status; and

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when copy restriction is enabled, preventing copying of the content signal.

12. The content controller of claim 7, wherein the memory further comprises operational instructions that cause the processing module to:

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provide an audio scrambling signal to an audio processing module when the at least one associated content control indicator compares unfavorably to the at least one content control setting.

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13. A video device comprises:

a tuner operably coupled to receive a content signal and to produce, therefrom, a digitized content signal;

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a video decoder operably coupled to receive the digitized content signal and to produce, therefrom, decoded video;

a graphics controller operably coupled to receive the decoded video and to provide,

10 therefrom, a video output, wherein the graphics controller includes:

a processing module; and

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memory operably coupled to the processing module, wherein the memory stores operational instructions that cause the processing module to (a) monitor at least one of the content signal, the digitized content signal, the decoded video, and the video output, wherein the at least one of the content signal, the digitized content signal, the decoded video, and the video output includes video content and at least one associated content control indicator; (b) compare the at least one associated content control indicator with at least one content control setting;

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when the at least one associated content control indicator compares unfavorably to the at least one content control setting: (c) control scrambling of at least a portion of the digitized content signal or the decoded video to produce scrambled video content; and (d) provide the scrambled video content as the video output.

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14. The video device of claim 13 further comprises an audio decoder operably coupled to the tuner, wherein the audio decoder decodes digital audio of the content signal to produce decoded audio, and wherein the memory further includes operational instructions that cause the processing module to:

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control scrambling of at least a portion of the digital audio or the decoded audio to produce scrambled audio content, such that the scrambled audio content is provided to an audio rendering device.

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15. The video device of claim 14, wherein the memory further comprises operational instructions that cause the processing module to control attenuation of the at least a portion of the digital audio or the decoded audio to produce the scrambled audio content.

10 16. The video device of claim 15 further comprises at least one of: a scramble module and an attenuation module operably coupled to the audio decoder.

17. The video device of claim 13, wherein the memory further comprises operational instructions that cause the processing module to:

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control scrambling at least a portion of closed captioned content to produce scrambled closed captioned content, wherein the content signal includes the closed captioned content; and

20 provide the scrambled closed captioned content as part of the video output.

18. The video device of claim 13, wherein the memory further comprises operational instructions that cause the processing module to:

25 interpret the at least one associated content control indicator to determine copy restriction status; and

when copy restriction is enabled, preventing copying of the video content.

19. The video device of claim 13, wherein the memory further comprises operational instructions that cause the processing module to:

5 provide an audio scrambling signal to an audio processing module when the at least one associated content control indicator compares unfavorably to the at least one content control setting.

10 20. The video device of claim 13 further comprises at least one of: a display and a recorder, wherein the display and the recorder are operably coupled to receive the video output.

15 21. The video device of claim 13 further comprises a scramble module operably coupled to scramble, when enabled, the at least a portion of the digitized content signal or the decoded video.

22. The video device of claim 13, wherein the graphics control further comprises a scramble module operably coupled to scramble, when enabled, the at least a portion of the digitized content signal or the decoded video.

METHOD AND APPARATUS FOR CONTROLLING DISPLAY OF CONTENT SIGNALS

Abstract of the Disclosure

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A method and apparatus for controlling display of content signals begins by receiving a content signal that includes video content and at least one associated content control indicator. The content signal may also include audio content associated with the video content. The processing continues by comparing the at least one associated content control indicator (e.g., a rating of mature subject matter of the content signal) with at least one content control setting (e.g., a parental setting based on allowable viewing of rated content signals). When the associated content indicator compares unfavorably to the content control setting, a video graphics processor scrambles the at least a portion of the video content. The scrambled video content is then provided to a video rendering device for subsequent display.

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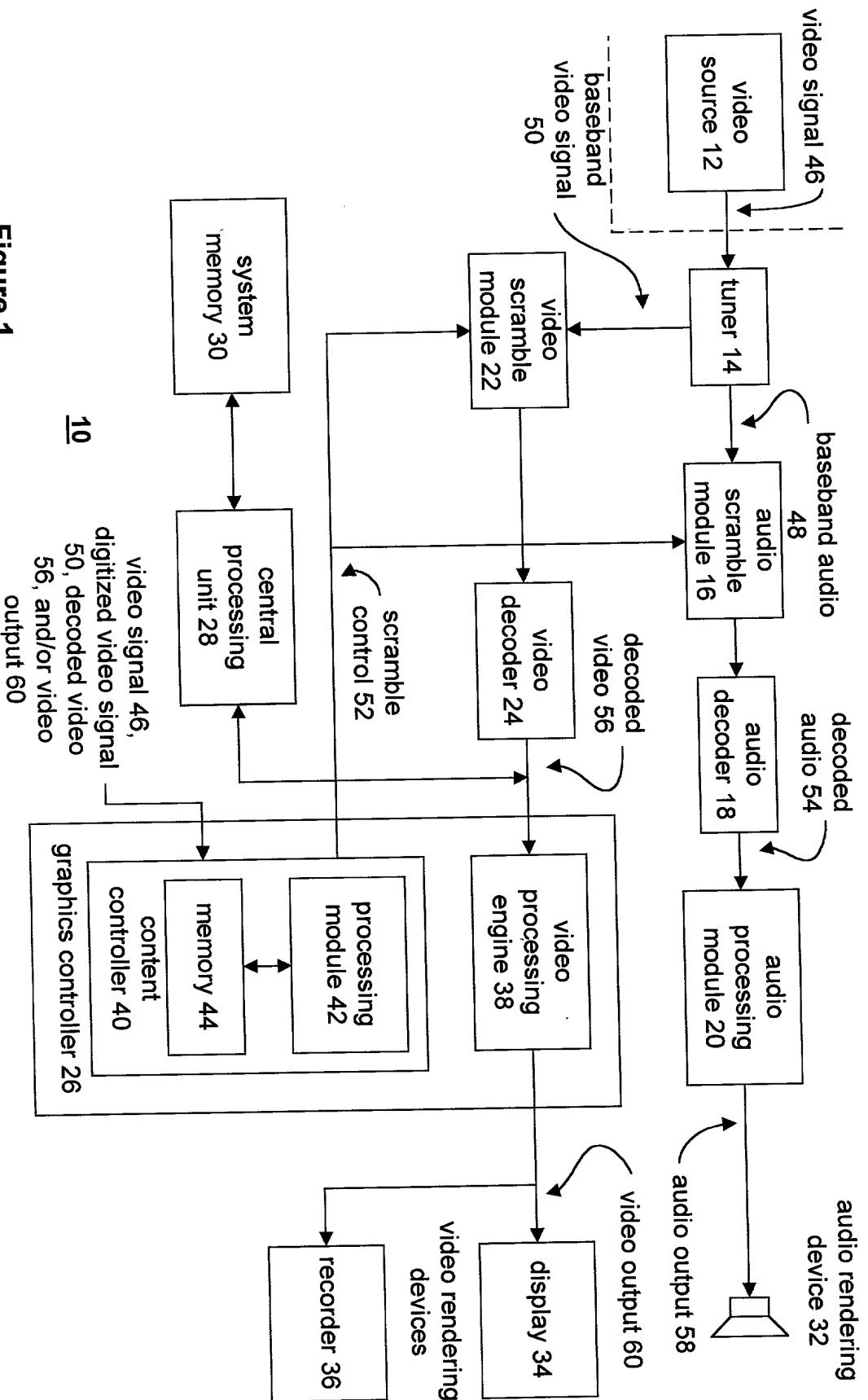


Figure 1

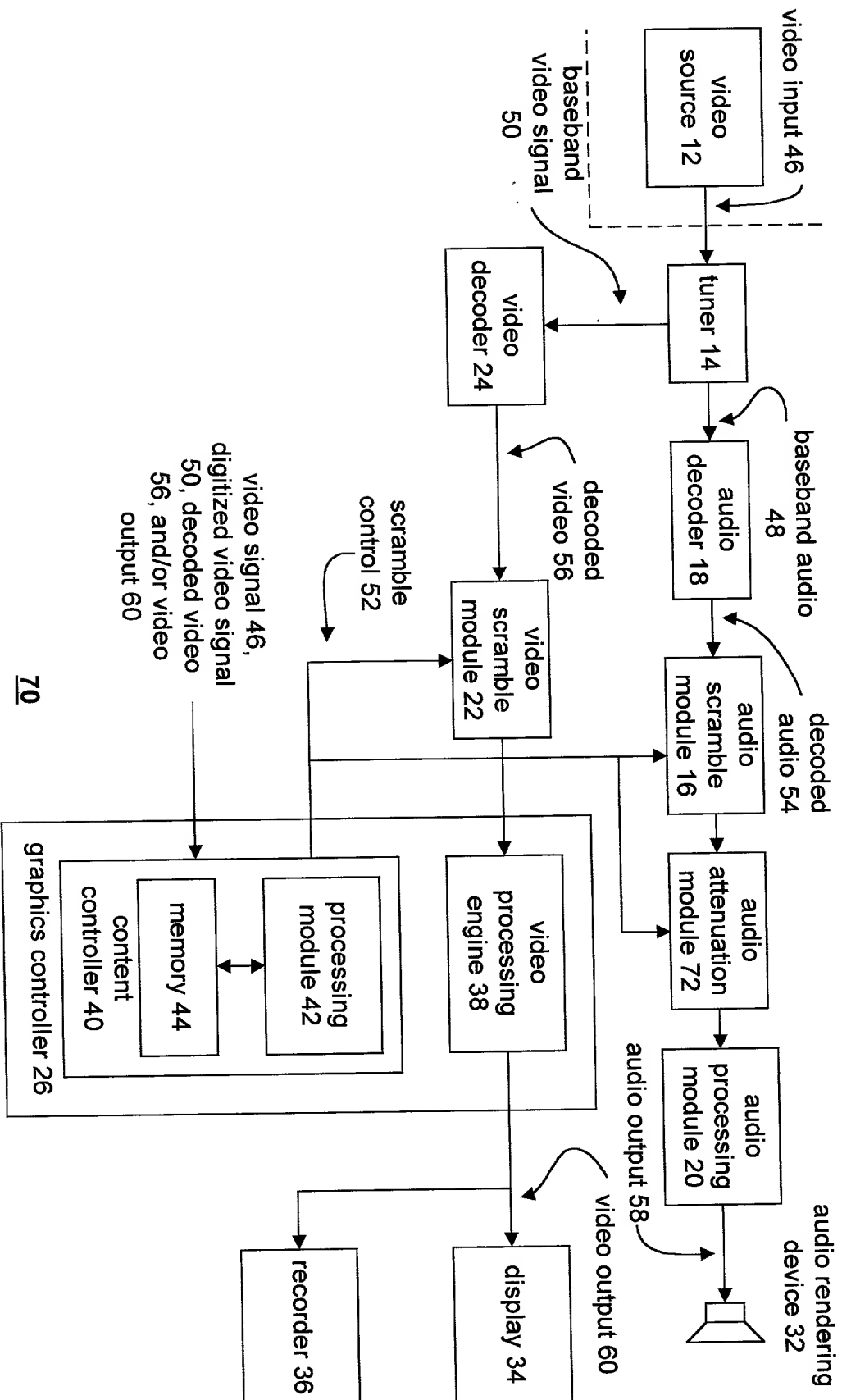


Figure 2

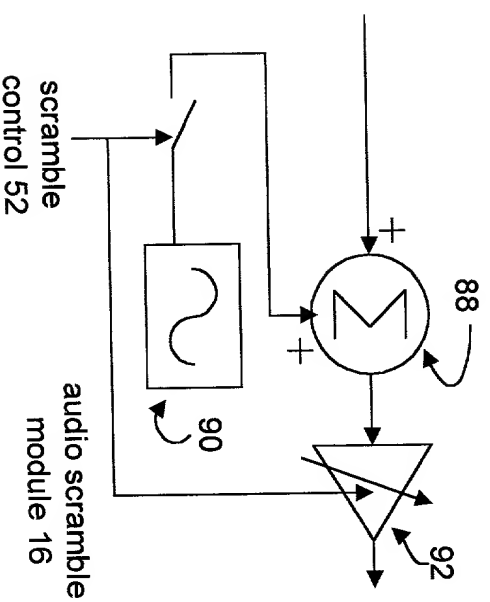
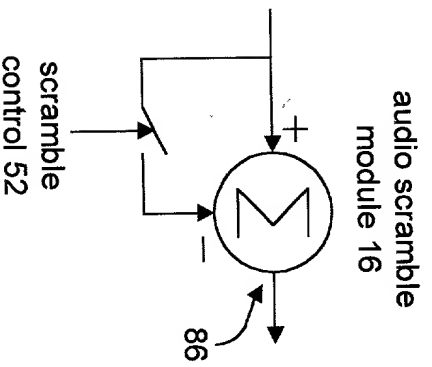
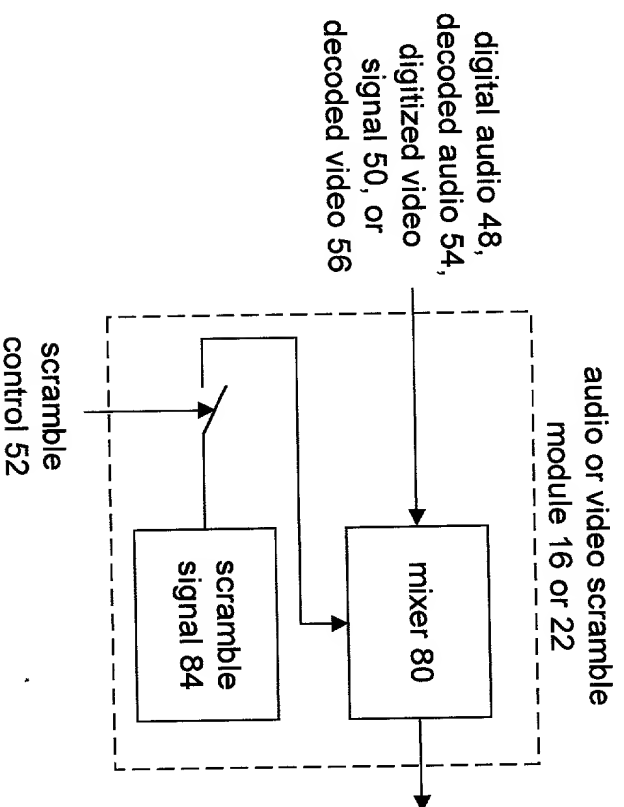
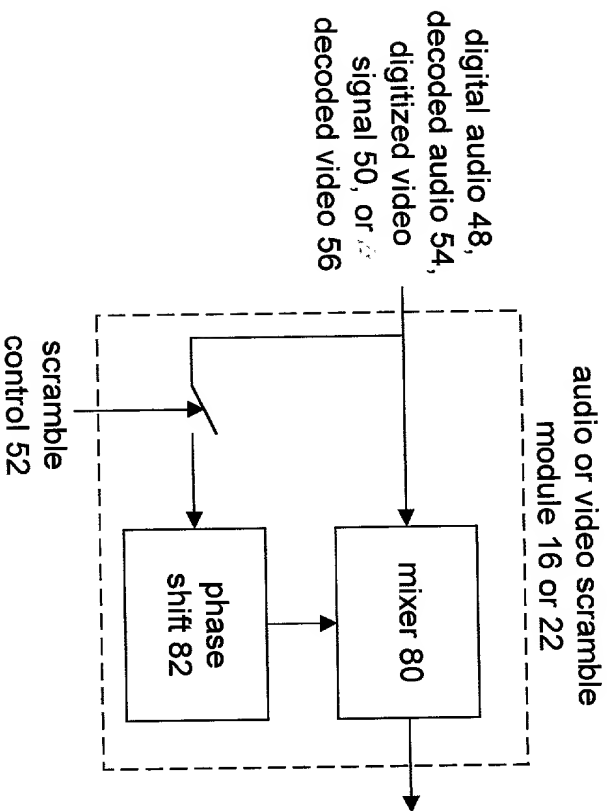
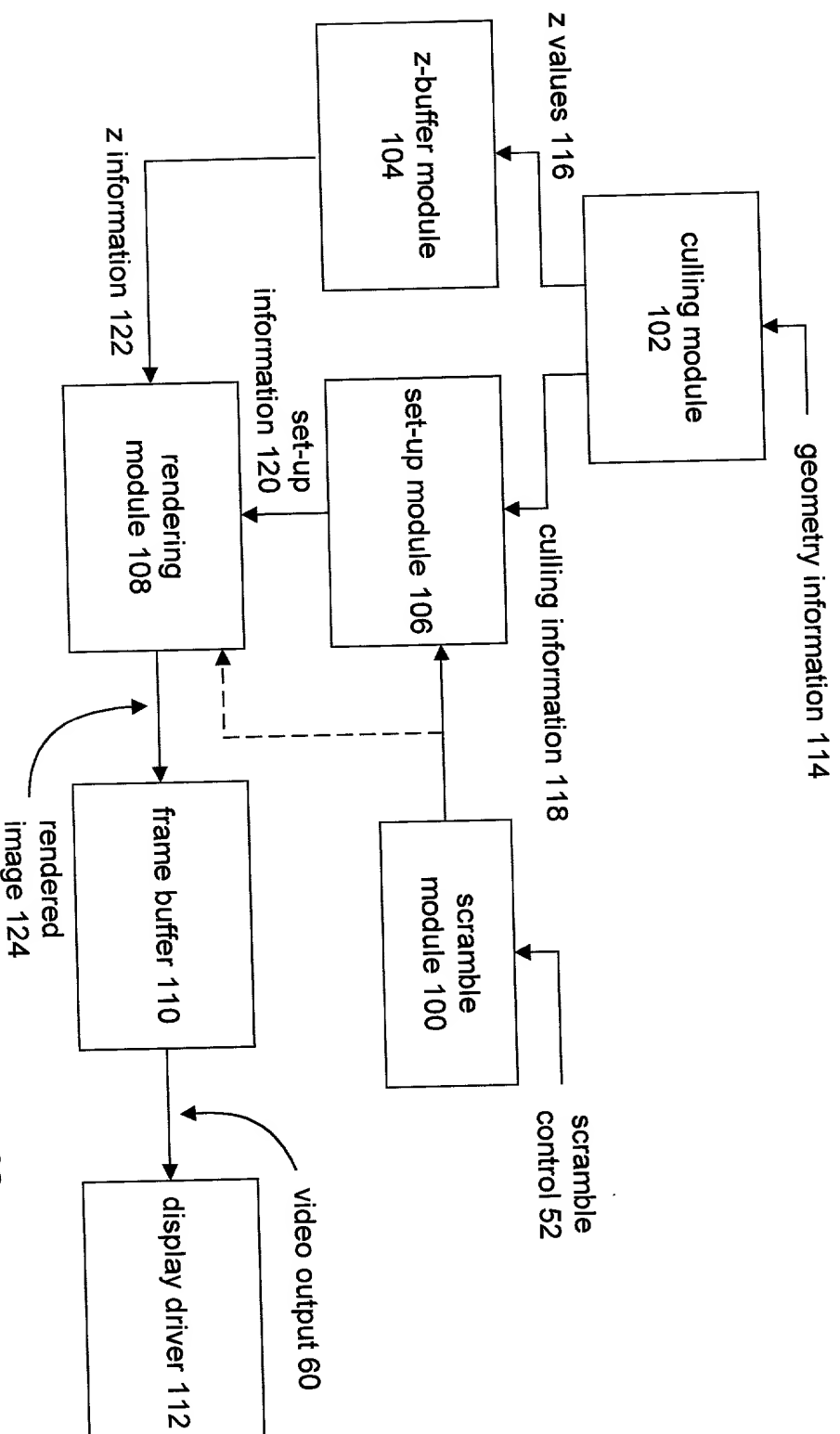


Figure 3



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Figure 4

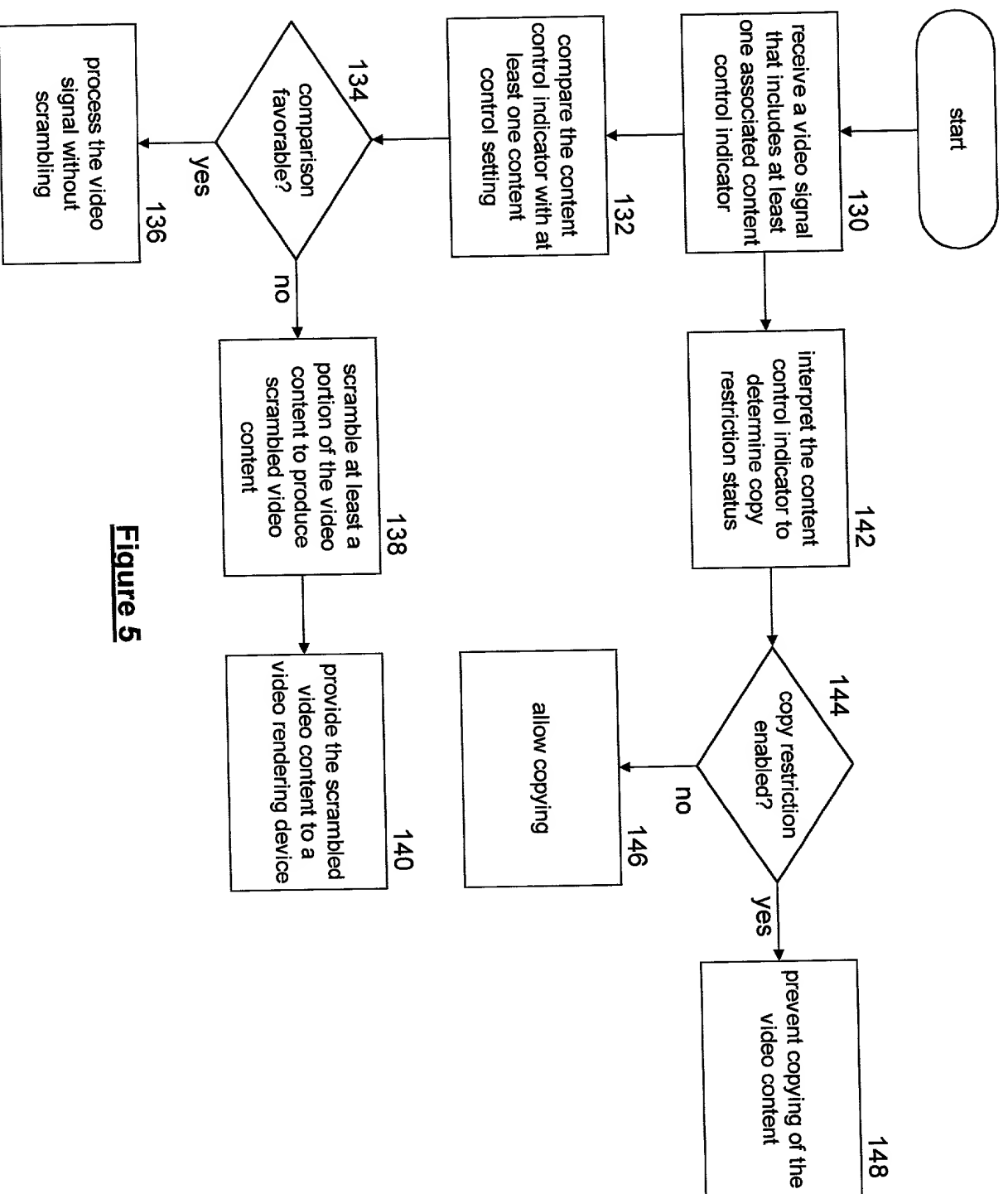


Figure 5

DECLARATION AND POWER OF ATTORNEY
Pursuant to 37 C.F.R 1.63 and 1.67

As a below named inventor, we each hereby declare that:
My residence, post office address and citizenship are as stated below next to my name; and
I believe that I am one of the original and first joint inventors of the subject matter of a patent application entitled: **METHOD AND APPARATUS FOR CONTROLLING DISPLAY OF CONTENT SIGNALS**. The specification for the patent application (check one):

<input checked="" type="checkbox"/>	is attached hereto.
<input type="checkbox"/>	was filed on _____ as Application Serial No. _____ and was amended on _____ (if applicable).
<input type="checkbox"/>	was filed as PCT International Application No. PCT/ _____ on _____ and was amended on _____ (if applicable).
<input type="checkbox"/>	was filed on _____ as Application Serial No. _____ and was issued a Notice of Allowance on _____

I hereby state that I have reviewed and understood the contents of the above identified patent application, including the claims as amended by any amendment referred to above or as allowed as indicated above.

I acknowledge the duty to disclose all information known to me to be material to the patentability of this patent application as defined in 37 C.F.R. Section 1.56 for the pendency of the application. If this is a continuation-in-part (CIP) application, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. Section 112, I acknowledge the duty to disclose to the Office all information known to me to be material to patentability of the application as defined in 37 C.F.R. Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this CIP application.

I hereby claim foreign priority benefits under 35 U.S.C. Sections 119 and 365 of any foreign application(s) for patent(s) or inventor's certificate(s) listed below. I have also identified below any foreign application(s) for patent(s) or inventor's certificate(s) filed by me or my assignee which:
disclose the subject matter claimed in this patent application; and
have a filing date that is either:

- (1) before the filing date of the application on which my priority is claimed; or,
- (2) before the filing date of this application when no priority is claimed:

Prior Foreign Patents

priority claimed	Number	Country	Mo/Day/Yr Filed	Date First Laid Open or Published	Date Granted or Patented
<input type="checkbox"/>					
<input type="checkbox"/>					
<input type="checkbox"/>					

I hereby claim the benefit under 35 U.S.C. Sections 120 and 365 of any United States application(s) listed below and PCT international application(s) listed below:

Prior U.S. or PCT Applications

Application No.	Mo/Day/Yr Filed	Status

Client No. 980090

I hereby appoint Timothy W. Markison, Registration No. 33,534, Christopher J. Reckamp, Registration No. 34,414, Paul Anderson, Patent Agent, Registration No. 39,896 Raffi Gostanian, Jr. Registration No. 42,595 all attorneys (or agents) with the firm of MARKISON & RECKAMP, P.C. having its principal place of business at 175 W. Jackson Blvd. - Suite 1015, Chicago, Illinois 60604, and Sally Daub, Patent Agent, Registration No. 41478 of ATI International SRL. as my attorneys, with full power of substitution and revocation, to prosecute this patent application and to transact all business in the United States Patent and Trademark Office connected therewith, and to file and prosecute any international patent applications filed thereon before any international authorities under the Patent Cooperation Treaty, and I hereby authorize them to act and rely on instructions from and communicate directly with the person/assignee/attorney/firm/ organization who/which first sent this case to them and by whom/which I hereby declare that I have consented after full disclosure to be represented unless/until I instruct them in writing to the contrary.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of this patent application or any patent issued thereon.

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